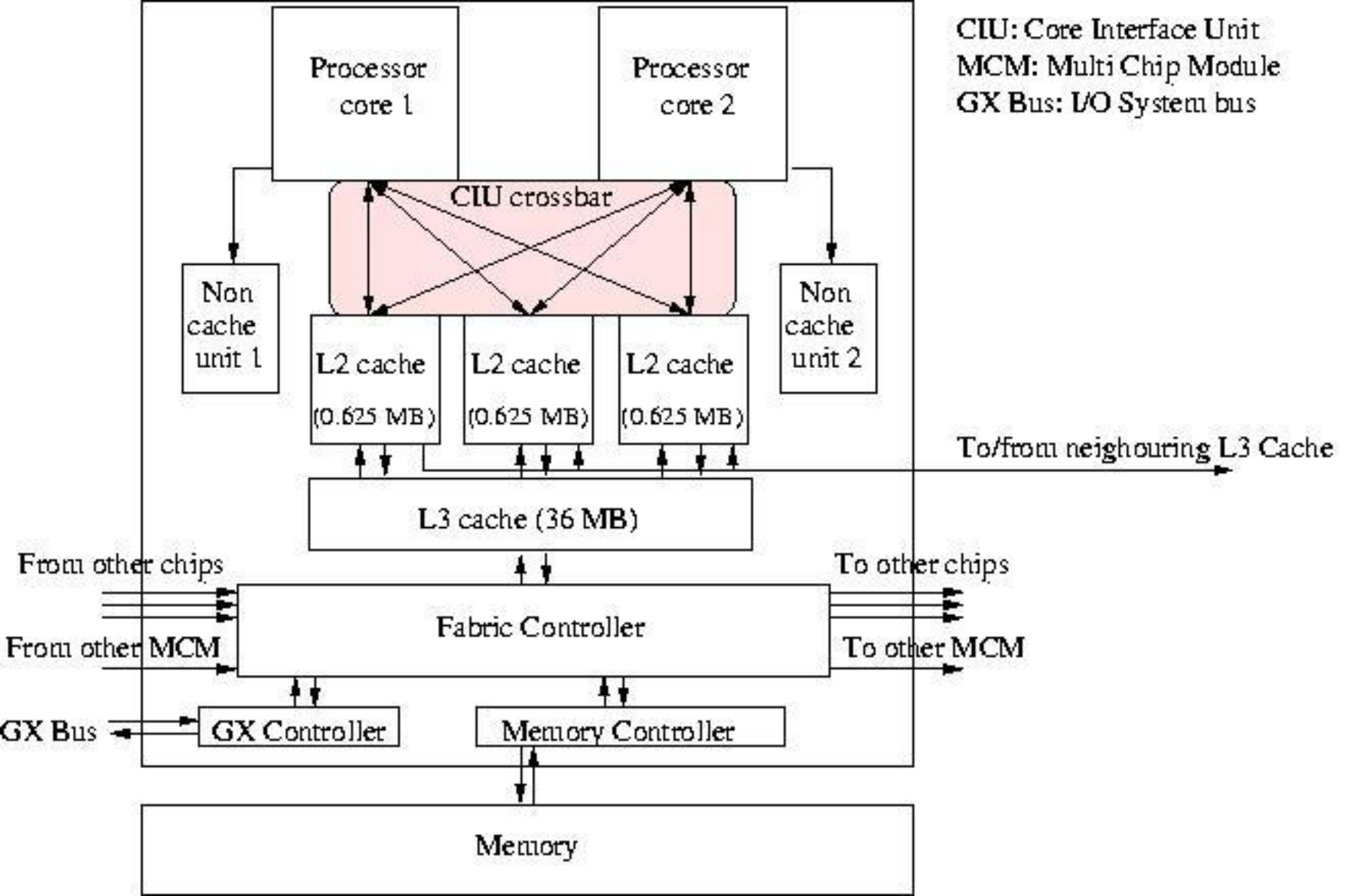
CIU: Core Interface Unit
MCM: Multi Chip Module
GX Bus: I/O System bus
Processor core 1
Processor core 2
CIU crossbar
Non cache unit 1
Non cache unit 2
L2 cache (0.625 MB)
L2 cache (0.625 MB)
L2 cache (0.625 MB)
To/from neighouring L3 Cache
L3 cache (36 MB)
From other chips
To other chips
Fabric Controller
From other MCM
To other MCM
GX Bus
GX Controller
Memory Controller
Memory